

ABSTRACT

There is provided a signal processing apparatus and a signal processing method, which can simultaneously perform reduction in jitter components and reduction in error rate.

A signal processing apparatus for processing a signal by a PRML method is provided with an A/D converter (4) for converting an analog signal into a digital signal; a first waveform equalizer (14) which is connected to the A/D converter (4), and amplifies a specific band of a signal to optimize data of a clock extraction system; a second waveform equalizer (15) which is connected to the A/D converter (4), and amplifies the specific band of the signal and performs waveform equalization to optimize data of a data processing system; a timing recovery logic circuit (11) which is connected to the first waveform equalizer (14), and extracts a reproduction clock; and a decoder (16) which is connected to the second waveform equalizer (15), and decodes data.